

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A LC-Display with n gate drivers and a source drivers for driving ~~a the~~ LC-Display with dots arranged in x rows and y columns, wherein the gate drivers ~~has~~ comprises several output stages for driving the gate lines of the displayLC-Display, ~~characterised in that, the LC-Display comprising:~~

a gate on supply line VH is ~~provided~~ to turn on a transistor of the LC-Display;

a gate off supply line VL is ~~provided~~ to turn off a transistor of the LC-Display,
wherein the gate off supply line VL;

an additional ~~voltage~~ gate off supply line VLclean is ~~provided~~ to substantially reduce a discharge time associated with driving transistors of the LC-Display, ~~which~~
wherein the additional gate off supply line is coupled to the output stages of the gate drivers and is routed as a separate track from the gate off supply line VL; and

a circuit to connect a storage capacitance Cst of a selected gate line GLy to the additional gate off supply line VLclean and to keep other storage capacitors Cst of unselected gate lines connected to the gate off supply line VL.

2. (currently amended) The LC-Display as claimed in claim 1, ~~whereas~~ wherein the output stage is ~~provided with~~ comprises:

a PMOS transistor and two NMOS transistors and the PMOS transistor is MP1
arranged between the gate on supply line VH and ~~the~~ an output of the output stage; and
the

a first NMOS transistor ~~MN1~~ MN1 is arranged between the gate off supply line VL and the output of the output stage; and

the a second NMOS transistor MN2 is arranged between the additional gate off supply line VLclean and the output of the output stage.

3. (currently amended) The LC-Display as claimed in claim 1, ~~whereas~~ wherein the additional gate off supply line VLclean is routed over a separate track from VL potential on the LC-Display glass.

4. (currently amended) The LC-Display as claimed in claim 1, ~~whereas~~ wherein the a track of the gate off supply line ~~W~~ and VL and the a track of the additional supply line VLclean are coupled to the a same supply level.

5. (currently amended) The LC-Display as claimed in claim 1, further comprising a power supply to supply a voltage to the gate off supply line VL and the additional gate off supply line VLclean, ~~whereas~~ wherein the a track of the gate off supply line VL and the a track of the additional gate off supply line (~~VLclean~~) VLclean are connected together in a location where the a track impedance to the supply circuit's output an output of the power supply is relatively low.

6. (currently amended) Method A method for driving a display with n gate drivers and at least one source driver, ~~whereas~~ the method comprising:
 - arranging dots are arranged in x rows and y columns;
 - providing the gate driver has several output stages for driving gate lines of the display;
 - providing a gate on supply line VH to turn on a transistor of the LC-Display;
 - providing a gate off supply line VL to turn off a transistor of the LC-Display;
 - providing an additional gate off supply line VLclean to substantially reduce a discharge time associated with driving transistors of the LC-Display, wherein the additional gate off supply line is coupled to the output stages of the gate drivers and is routed as a separate track from the gate off supply line VL;
 - connecting and a capacitance of the of a selected gate line is connected to the to a previous gate line; and
 - activating characterised in that, an additional supply line VLclean of the output

stage for ~~row~~the selected gate line is activated with the supply line VL when the previous gate line ~~row~~ is activated.

7. (currently amended) ~~Method~~A method for driving a display with n gate drivers and a source driver, ~~whereas the method comprising:~~
arranging dots are arranged in x rows and y columns;
providing the gate driver has several output stages for driving the gate lines of the display;

providing a gate on supply line VH to turn on a transistor of the LC-Display;
providing a gate off supply line VL to turn off a transistor of the LC-Display;
providing an additional gate off supply line VLclean to substantially reduce a discharge time associated with driving transistors of the LC-Display, wherein the additional gate off supply line is coupled to the output stages of the gate drivers and is routed as a separate track from the gate off supply line VL;

connecting and a capacitance of the of a selected gate line is connected to the to a next gate line; and

activating characterised in that, an additional supply line VLclean of the output stage for ~~row~~the selected gate line is activated with the supply line VL when the next gate line is activated.